

FGW60N65WD

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Discrete IGBT

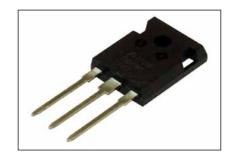
Discrete IGBT (High-Speed W series) 650V / 60A

Features

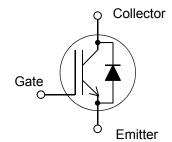
Low power loss Low switching surge and noise High reliability, high ruggedness (RBSOA, SCSOA etc.)

Applications

Uninterruptible power supply PV Power coditionner Inverter welding machine



Equivalent circuit



Maximum Ratings and Characteristics

● Absolute Maximum Ratings (at T_c=25°C unless otherwise specified)

Items	Symbols	Characteristics	Units	Remarks
Collector-Emitter Voltage	Vces	650	V	
Gate-Emitter Voltage	V _{GES}	±20	٧	
Transient Gate-Emitter Voltage	V GES	±30		Tp<1µs
DC Collector Current	Ic@25	83	Α	Tc=25°C
DC Collector Current	Ic@100	60	Α	Tc=100°C
Pulsed Collector Current	I _{CP}	240	Α	Note *1
Turn-Off Safe Operating Area		240	Α	Vce≤650V
	-	240	Υ.	T _j ≤175°C
Diode Forward Current	IF@25	45	Α	
	I _{F@100}	30	Α	
Diode Pulsed Current	I _{FP}	240	Α	Note *1
IGBT Max. Power Dissipation	P _{D_IGBT}	405	W	Tc=25°C
FWD Max. Power Dissipation	P _{D_FWD}	105	W	Tc=25°C
Operating Junction Temperature	T _i	-40 ~ +175	°C	
Storage Temperature	T _{stg}	-55 ~ +175	°C	

Note *1 : Pulse width limited by T_{jmax} .

● Electrical characteristics (at T_j= 25°C unless otherwise specified)

Description	Symbols	Symbols Conditions		Characteristics			Units
Description	Symbols	Conditions		min.	typ.	max.	Units
Zero Gate Voltage Collector Current	Ices	V _{CE} = 650V, V _{GE} = 0V	T _j =25°C	-	-	250	μΑ
Zero Gate voltage Collector Current	ICES	V CE - 050 V, V GE - 0 V	T _j =175°C	-	-	2	mΑ
Gate-Emitter Leakage Current	Iges	$V_{CE} = 0V$, $V_{GE} = \pm 20V$		-	-	200	nA
Gate-Emitter Threshold Voltage	V _{GE (th)}	$V_{CE} = 20V, I_{C} = 60mA$		3.0	4.0	5.0	V
			T _j =25°C	1.40	1.80	2.20	
Collector-Emitter Saturation Voltage	V _{CE} (sat)	$V_{GE} = 15V$, $I_{C} = 60A$	T _j =125°C	-	2.05	-	V
			T _j =175°C	-	2.10	-	
Input Capacitance	Cies	Vce=25V		2150	4300	6450	
Output Capacitance	Coes	V _{GE} =0V		63	125	188	pF
Reverse Transfer Capacitance	Cres	f=1MHz		48	95	143	
		Vcc = 520V					
Gate Charge	Q _G	Ic = 60A		125	250	375	nC
		V _{GE} = 15V					
Turn-On Delay Time	t _{d(on)}	T ₁ = 25°C, V _{CC} = 400V		15	29	44	
Rise Time	t	I ₁ = 25 C, V _{CC} = 400V	20	40	60	ns mJ	
Turn-Off Delay Time	t _{d(off)}		130	260	390		
Fall Time	tr	R_{G} = 10 Ω , L = 500 μ H Energy loss include "tail" and FWD reverse recovery.		39	78		117
Turn-On Energy	Eon			0.30	0.60		0.90
Turn-Off Energy	Eoff			0.34	0.67		1.01
Turn-On Delay Time	t _{d(on)}	$T_{\rm j}$ = 150°C, $V_{\rm CC}$ = 400V $I_{\rm c}$ = 30A, $V_{\rm eE}$ = 15V $R_{\rm G}$ = 10 Ω , L = 500 μ H Energy loss include "tail" and FWD reverse		15	29	44	ns
Rise Time	t			20	40	60	
Turn-Off Delay Time	t _{d(off)}			148	295	443	
Fall Time	tr			34	68	102	
Turn-On Energy	Eon			0.48	0.96	1.44	I
Turn-Off Energy	Eoff	recovery.	0.37	0.73	1.10	mJ	
			T _j =25°C	1.8	2.5	3.2	V
Forward Voltage Drop	VF	I _F =25A	T _j =125°C	-	1.9	-	V
- '			T _j =175°C	-	1.7	-	V
Diode Reverse Recovery Time	trr	Vcc=400V, I _F =30A		38	75	113	ns
Diode Reverse Recovery Charge	Qrr	-di _F /dt=500A/µs, T _j =25°C		0.15	0.30	0.45	μC
Diode Reverse Recovery Time	trr	Vcc=400V, I _F =30A		53	105	158	ns
Diode Reverse Recovery Charge	Qrr	-di _F /dt=500A/µs, T _i =150°0	3	0.45	0.90	1.35	μC

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● Thermal resistance characteristics

Items	Symbols Conditions	Characteristics			Units	
Itellis		Conditions	min.	typ.	max.	Uiiiis
Thermal Resistance, Junction-Ambient	R _{th(j-a)}	-	-	-	50	
Thermal Resistance, IGBT Junction to Case	R _{th(j-c)_IGBT}	-	-	-	0.366	°C/W
Thermal Resistance, FWD Junction to Case	R _{th(j-c)_FWD}	-	-	-	1.389	

20

0

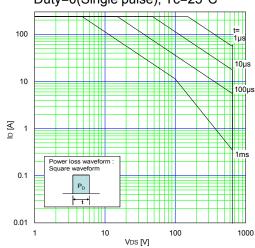
50

Characteristics (Representative) Graph.1

DC Collector Current vs Tc
VGE≥+15V,Tj≤175°C

120
100
100
40
100
40

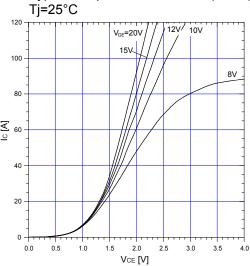
Graph.2 FBSOA Duty=0(Single pulse), Tc=25°C



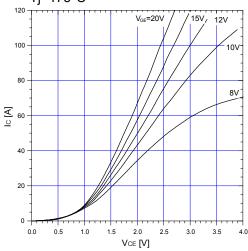
Graph.3
Typical Output Characteristics (VcE-Ic)
Ti=25°C

100

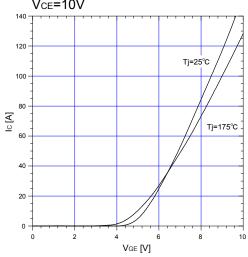
Case Temperature [°C]



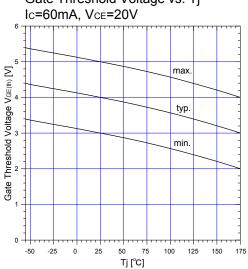
Graph.4
Typical Output Characteristics (VcE-Ic)
Tj=175°C



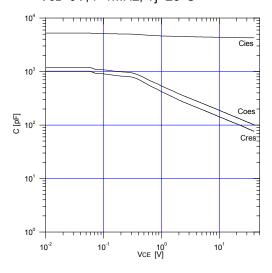
Graph.5
Typical Transfer Characteristics
Vc=10V



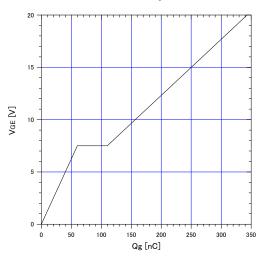
Graph.6
Gate Threshold Voltage vs. Tj



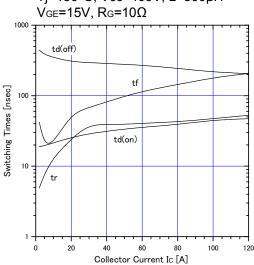
Graph.7 Typical Capacitance V_{GE}=0V, f=1MHz, Tj=25°C



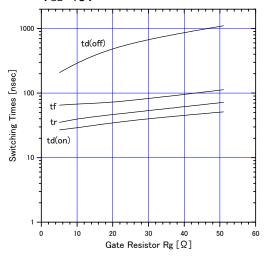
Graph.8
Typical Gate Charge
Vcc=520V, Ic=60A, Tj=25°C



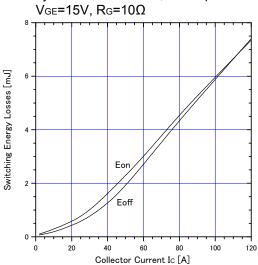
Graph.9
Typical switching time vs. Ic
Tj=150°C, Vcc=400V, L=500μH
VGE=15V, RG=100



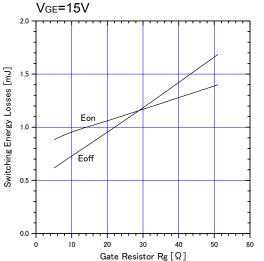
Graph.10
Typical switching time vs. Rg
Tj=150°C, Vcc=400V, Ic=30A, L=500μH
VGE=15V



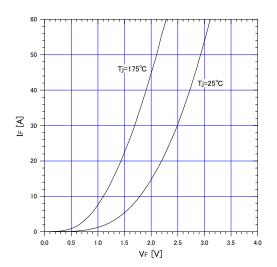
Graph.11
Typical switching losses vs. Ic
Tj=150°C, Vcc=400V, L=500µH
VGE=15V, RG=100



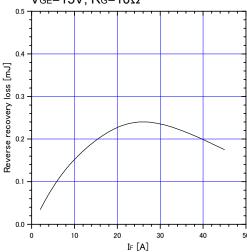
Graph.12
Typical switching losses vs. Rg
Tj=150°C, Vcc=400V, Ic=30A, L=500μH



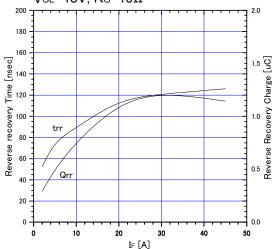
Graph.13 FWD Forward voltage drop (V_F-I_F)



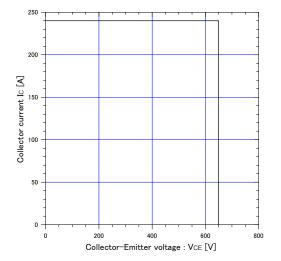
Graph.15 Typical reverse recovery loss vs. I_F Tj=150°C, Vcc=400V, L=500 μ H VgE=15V, Rg=10 Ω



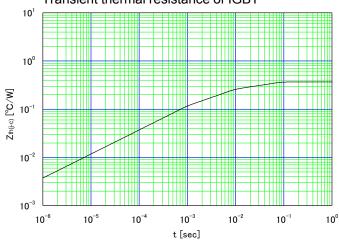
Graph.14 Typical reverse recovery characteristics vs. I_F Tj=150°C, Vcc=400V, L=500 μ H V_{GE}=15V, R_G=10 Ω



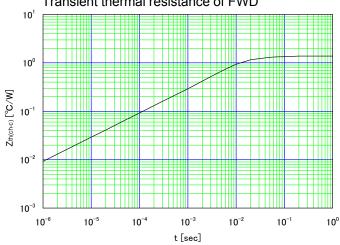
Graph.16 Reverse biased Safe Operating Area Tj≤175°C, V_{GE}=+15V/0V, R_G=10Ω







Graph.18
Transient thermal resistance of FWD



17,52 ±0.15

Solder

Plating

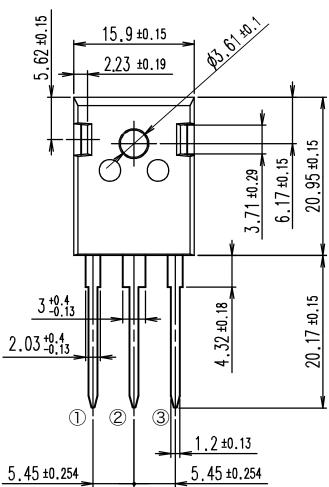
5.03 ±0.15

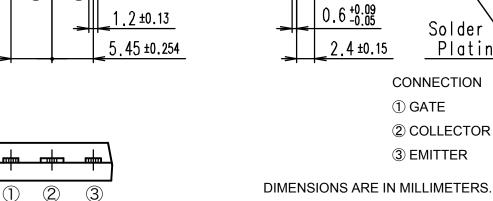
1.98 ±0.15

14.02 ±0.13

Outline Drawings, mm

Outview: TO-247 Package





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